

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

First Named Inventor:	FUKUDA, KEIKO	Art Unit:	4125
Appln. No.:	10/584,395	Examiner:	Cole, Brandon S.
Filed:	June 23, 2006	Conf. No.:	9000

For: VOLTAGE GENERATION CIRCUIT AND  
SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

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RESPONSE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed on February 4,  
2008, Applicants offer the following Remarks.

**Remarks** begin on page 2 of this Response.